



Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((((decod\*, convert\*, conversion\*, translat\*) &lt;near/6&gt; (microinstruction\*, instruction\*, c..."

Your search matched 27 of 96 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail

## » Search Options

[View Session History](#)
[New Search](#)

## » Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

## Modify Search


☐ Check to search only within this results set

 Display Format: ☒ Citation ☐ Citation & Abstract

 [Select All](#) [Deselect All](#)

- ☐ 1. **VLSI design and implementation of WCDMA channel decoder**  
 Xu Youyun; Li Zongwang; Ruan Ming; Luo Hanwen; Song Wentao;  
[Electrical and Computer Engineering, 2001. Canadian Conference on](#)  
 Volume 1, 13-16 May 2001 Page(s):241 - 245 vol.1  
 Digital Object Identifier 10.1109/CCECE.2001.933690  
[AbstractPlus](#) | Full Text: [PDF](#)(308 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 2. **A 3.33Gb/s (1200,720) low-density parity check code decoder**  
 Chien-Ching Lin; Kai-Li Lin; Hsie-Chia Chang; Chen-Yi Lee;  
[Solid-State Circuits Conference, 2005. ESSCIRC 2005. Proceedings of the 31st European](#)  
 12-16 Sept. 2005 Page(s):211 - 214  
 Digital Object Identifier 10.1109/ESSCIR.2005.1541597  
[AbstractPlus](#) | Full Text: [PDF](#)(387 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 3. **80960-next generation**  
 Hinton, G.;  
[COMPCON Spring '89. Thirty-Fourth IEEE Computer Society International Conference: Intellectual](#)  
[Papers.](#)  
 27 Feb.-3 March 1989 Page(s):13 - 17  
 Digital Object Identifier 10.1109/COMPCON.1989.301896  
[AbstractPlus](#) | Full Text: [PDF](#)(320 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 4. **A sub-word-parallel Galois field multiply-accumulate unit for digital signal processors**  
 Roy, S.;  
[Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on](#)  
 23-26 May 2005 Page(s):1626 - 1629 Vol. 2  
 Digital Object Identifier 10.1109/ISCAS.2005.1464915  
[AbstractPlus](#) | Full Text: [PDF](#)(184 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 5. **A highly efficient domain-programmable parallel architecture for iterative LDPC decoding**  
 Al-Rawi, G.; Cioffi, J.;  
[Information Technology: Coding and Computing, 2001. Proceedings. International Conference on](#)  
 2-4 April 2001 Page(s):569 - 577  
 Digital Object Identifier 10.1109/ITCC.2001.918858

[AbstractPlus](#) | Full Text: [PDF\(956 KB\)](#) IEEE CNF  
[Rights and Permissions](#)

- ☐ 6. **Loosely coupled memory-based decoding architecture for low density parity check codes**  
Se-Hyeon Kang; In-Cheol Park;  
[Circuits and Systems I: Regular Papers, IEEE Transactions on \[see also Circuits and Systems I: F and Applications, IEEE Transactions on\]](#)  
Volume 53, Issue 5, May 2006 Page(s):1045 - 1056  
Digital Object Identifier 10.1109/TCSI.2005.862181  
  
[AbstractPlus](#) | Full Text: [PDF\(880 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
  
- ☐ 7. **Loosely coupled memory-based decoding architecture for low density parity check codes**  
Se-Hyeon Kang; In-Cheol Park;  
[Custom Integrated Circuits Conference, 2005. Proceedings of the IEEE 2005](#)  
18-21 Sept. 2005 Page(s):703 - 706  
Digital Object Identifier 10.1109/CICC.2005.1568765  
  
[AbstractPlus](#) | Full Text: [PDF\(265 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 8. **Implementation of an LDPC decoder on a vector signal processor**  
Lechner, G.; Bolzer, A.; Sayir, J.; Rupp, M.;  
[Signals, Systems and Computers, 2004. Conference Record of the Thirty-Eighth Asilomar Conference](#)  
Volume 1, 7-10 Nov. 2004 Page(s):549 - 553 Vol.1  
Digital Object Identifier 10.1109/ACSSC.2004.1399193  
  
[AbstractPlus](#) | Full Text: [PDF\(564 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 9. **A new bidirectional algorithm for decoding trellis codes**  
Senk, V.; Radivojac, P.;  
[EUROCON'2001, Trends in Communications, International Conference on](#)  
Volume 1, 4-7 July 2001 Page(s):34 - 36 vol.1  
Digital Object Identifier 10.1109/EURCON.2001.937757  
  
[AbstractPlus](#) | Full Text: [PDF\(320 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ 10. **A new family of unitary space-time codes with a fast parallel sphere decoder algorithm**  
Xinjia Chen; Kemin Zhou; Aravena, J.L.;  
[Information Theory, IEEE Transactions on](#)  
Volume 52, Issue 1, Jan. 2006 Page(s):115 - 140  
Digital Object Identifier 10.1109/TIT.2005.860421  
  
[AbstractPlus](#) | Full Text: [PDF\(768 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
  
- ☐ 11. **Two-step trellis decoding of partial unit memory convolutional codes**  
Hole, M.F.; Ytrehus, O.;  
[Information Theory, IEEE Transactions on](#)  
Volume 43, Issue 1, Jan. 1997 Page(s):324 - 330  
Digital Object Identifier 10.1109/18.567735  
  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(740 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
  
- ☐ 12. **In-system FPGA prototyping of an Itanium microarchitecture**  
Wunderlich, R.E.; Hoe, J.C.;  
[Computer Design: VLSI in Computers and Processors, 2004. ICCD 2004. Proceedings, IEEE International](#)  
11-13 Oct. 2004 Page(s):288 - 294  
Digital Object Identifier 10.1109/ICCD.2004.1347935

[AbstractPlus](#) | Full Text: [PDF\(471 KB\)](#) IEEE CNF  
[Rights and Permissions](#)

- ☐ 13. **A VLIW architecture for logarithmic arithmetic**  
Arnold, M.G.;  
[Digital System Design, 2003. Proceedings. Euromicro Symposium on](#)  
1-6 Sept. 2003 Page(s):294 - 302  
Digital Object Identifier 10.1109/DSD.2003.1231957  
[AbstractPlus](#) | Full Text: [PDF\(379 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 14. **An optimized processor for fast Reed-Solomon encoding and decoding**  
Ji, H.M.;  
[Acoustics, Speech, and Signal Processing, 2002. Proceedings. \(ICASSP '02\). IEEE International C](#)  
Volume 3, 13-17 May 2002 Page(s):III-3097 - III-3100 vol.3  
Digital Object Identifier 10.1109/ICASSP.2002.1005342  
[AbstractPlus](#) | Full Text: [PDF\(485 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 15. **Joint encoding and decoding methods for digital audio broadcasting of multiple programs**  
Naik, N.; Sinha, D.; Sundberg, C.-E.W.; Tracey, J.;  
[Broadcasting, IEEE Transactions on](#)  
Volume 51, Issue 4, Dec. 2005 Page(s):439 - 448  
Digital Object Identifier 10.1109/TBC.2005.855116  
[AbstractPlus](#) | Full Text: [PDF\(1032 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ 16. **Linear distances as branch metrics for Viterbi decoding of trellis codes**  
Hui-Ling Lou;  
[Acoustics, Speech, and Signal Processing, 2000. ICASSP '00. Proceedings, 2000 IEEE Internation](#)  
Volume 6, 5-9 June 2000 Page(s):3267 - 3270 vol.6  
Digital Object Identifier 10.1109/ICASSP.2000.860097  
[AbstractPlus](#) | Full Text: [PDF\(304 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 17. **A study of dynamic scheduling techniques for multiscalar processors**  
Madavarapu, V.K.; Franklin, M.; Sundararaman, K.K.;  
[High Performance Computing, 1996. Proceedings. 3rd International Conference on](#)  
19-22 Dec. 1996 Page(s):413 - 418  
Digital Object Identifier 10.1109/HIPC.1996.565856  
[AbstractPlus](#) | Full Text: [PDF\(468 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 18. **A CCITT standard 32 kbit/s ADPCM LSI codec**  
Nishitani, T.; Kuroda, I.; Satoh, M.; Katoh, T.; Aoki, Y.;  
[Acoustics, Speech, and Signal Processing \[see also IEEE Transactions on Signal Processing\], IEE](#)  
Volume 35, Issue 2, Feb 1987 Page(s):219 - 225  
[AbstractPlus](#) | Full Text: [PDF\(928 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ 19. **An error-compensation A/D conversion technique**  
Yung, H.T.; Chao, K.S.;  
[Circuits and Systems, IEEE Transactions on](#)  
Volume 38, Issue 2, Feb. 1991 Page(s):187 - 195  
Digital Object Identifier 10.1109/31.68296  
[AbstractPlus](#) | Full Text: [PDF\(864 KB\)](#) IEEE JNL  
[Rights and Permissions](#)

- ☐ 20. **Design of a high-throughput low-power IS95 Viterbi decoder**  
Xun Liu; Papaefthymiou, M.C.;  
[Design Automation Conference, 2002. Proceedings, 39th](#)  
10-14 June 2002 Page(s):263 - 268  
Digital Object Identifier 10.1109/DAC.2002.1012633  
[AbstractPlus](#) | Full Text: [PDF](#)(792 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 21. **A 250 MHz single-chip multiprocessor for A/V signal processing**  
Koyama, T.; Iwata, E.; Yoshikawa, H.; Hanaki, H.; Hasegawa, K.; Aoki, M.; Yasue, M.; Schronbenh  
Kumata, I.; Koyanagi, H.;  
[Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC, 2001 IEEE International](#)  
5-7 Feb. 2001 Page(s):146 - 147, 441  
Digital Object Identifier 10.1109/ISSCC.2001.912580  
[AbstractPlus](#) | Full Text: [PDF](#)(208 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 22. **A high speed microprocessor core for the embedded applications-PKURS001**  
Wang Yingchun; Ji Lijiu; Han Shu; Liu Ling; Wang Yangyuan;  
[ASIC, 2001. Proceedings, 4th International Conference on](#)  
23-25 Oct. 2001 Page(s):748 - 751  
Digital Object Identifier 10.1109/ICASIC.2001.982671  
[AbstractPlus](#) | Full Text: [PDF](#)(267 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 23. **Micro-architecture of the VAX 9000**  
Murray, J.E.; Salett, R.M.; Hetherington, R.C.; McKeen, F.X.;  
[Comcon Spring '90. 'Intellectual Leverage'. Digest of Papers. Thirty-Fifth IEEE Computer Society](#)  
[Conference.](#)  
26 Feb.-2 March 1990 Page(s):44 - 53  
Digital Object Identifier 10.1109/CMPCON.1990.63652  
[AbstractPlus](#) | Full Text: [PDF](#)(764 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 24. **FLUX chip: design of a 20-GHz 16-bit ultrapipelined RSFQ processor prototype based on 1.7 technology**  
Dorojevets, M.; Bunyk, P.; Zinoviev, D.;  
[Applied Superconductivity, IEEE Transactions on](#)  
Volume 11, Issue 1, Part 1, March 2001 Page(s):326 - 332  
Digital Object Identifier 10.1109/77.919349  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(756 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ 25. **From sequences of dependent instructions to functions: an approach for improving perform speculation**  
Yehia, S.; Temam, O.;  
[Computer Architecture, 2004. Proceedings, 31st Annual International Symposium on](#)  
19-23 June 2004 Page(s):238 - 249  
Digital Object Identifier 10.1109/ISCA.2004.1310778  
[AbstractPlus](#) | Full Text: [PDF](#)(459 KB) IEEE CNF  
[Rights and Permissions](#)



Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((((decod\*, convert\*, conversion\*, translat\*) &lt;near/6&gt; (microinstruction\*, instruction\*, c..."



Your search matched 27 of 96 documents.

A maximum of 27 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

((((decod\*, convert\*, conversion\*, translat\*) &lt;near/6&gt; (microinstruction\*, instruction

Search &gt;

☐ Check to search only within this results set

Display Format:

☒ Citation☐

Citation &amp; Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

view selected items

[Select All](#) [Deselect All](#)**26. Higher performance and lower power enhancements to VLIW architectures**

Gass, W.;

[Signal Processing Systems, 2001 IEEE Workshop on](#)

26-28 Sept. 2001 Page(s):157

Digital Object Identifier 10.1109/SIPS.2001.957342

[AbstractPlus](#) | Full Text: [PDF](#)(45 KB) IEEE CNF[Rights and Permissions](#)**27. A mainframe processor in CMOS technology with 0.5  $\mu$ m channel length**

Schettler, H.; Hajdu, J.; Getzlaff, K.; Loehlein, W.-D.; Starke, C.-W.;

[Solid-State Circuits Conference, 1990. Digest of Technical Papers, 37th ISSCC., 1990 IEEE Intern.](#)

14-16 Feb. 1990 Page(s):50 - 51

Digital Object Identifier 10.1109/ISSCC.1990.110124

[AbstractPlus](#) | Full Text: [PDF](#)(276 KB) IEEE CNF[Rights and Permissions](#)Indexed by  
 Inspec®[Help](#) [Contact Us](#) [Privacy](#)

© Copyright 2006 IE